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Substitute for form 1449A/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	
		Filing Date	
		First Named Inventor	MOMTAZ, AFSHIN
		Group Art Unit	
		Examiner Name	
Sheet 1	of 2	Attorney Docket Number	019717-003100US

10979 U.S. PTO
10/09/06



U.S. PATENT DOCUMENTS						
Examiner Initials *	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
LW	AA	8,115,154		Antoniades et al.	09/05/00	
LW	AB	US 6,222,668	B1	Dutrisac et al.	04/24/01	
	AC					
	AD					
	AE					
	AF					
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	AJ					
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	AR					
	AS					
	AT					

Examiner Signature		Date Considered	9/20/05
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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Complete if Known	
	Application Number		
	Filing Date		
	First Named Inventor	MOMTAZ, AFSHIN	
	Group Art Unit		
	Examiner Name		
	Attorney Docket Number	019717-003100US	
Sheet	2	of	2

10/09/02
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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
LW	AU	ALEXANDER GILMAN et al., "Monolithic Clock and Data Recovery Chip for 10Gb/s Fiber Communication Systems," Final Report 1999-2000 for MICRO Project 99-078, Industrial Sponsor: Conexant Systems, Department of Electrical and Computer Engineering, University of California, Santa Barbara, California USA	
LW	AV	RICHARD C. WALKER, et al., "A 2.488 GB/s Si-Bipolar Clock and Data Recovery IC with Robust Loss of Signal Detection," HP Design Conference, Tokyo, 1997, Hewlett Packard Laboratories, Palo Alto, California USA	
	AW		
	AX		
	AY		
	AZ		
	AAA		
	ABB		
	ACC		
	ADD		
	AEE		

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FORM PTO/SB/08A/B, (10-01)
Substitute for PTO-1449A/B

INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

(use as many sheets as necessary)

Attorney Docket Number 50956/RJP/B600

Application Number 10/092,166

Filing Date March 4, 2002

Applicant(s) Afshin Momtaz, et al.

Group Art Unit 2816

Examiner Name Not Assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	DOCUMENT NUMBER Number - Kind Code ² (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶ (✓)
LW		EP 0 926 854 A2	06-30-1999	Barnard, Chris Wilhelm et al.	
LW		EP 0 930 742 A2	07-21-1999	Bartolo, Gabriele	

OTHER DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
W		RICHARD C. WALKER, et al., "A 2.488 Gb/s Si-Bipolar Clock and Data Recovery IC with Robust Loss of Signal Detection," <i>HP Design Conference, Tokyo 1997</i> , Hewlett Packard Laboratories, Palo Alto, California

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